

## Electronic Patent Application Fee Transmittal

<b>Application Number:</b>	10716309			
<b>Filing Date:</b>	17-Nov-2003			
<b>Title of Invention:</b>	Techniques for grouping circuit elements into logic blocks			
First Named Inventor/Applicant Name:	Ketan Padalia			
<b>Filer:</b>	David Brian Raczkowski/Lisa Jeanetta			
<b>Attorney Docket Number:</b>	015114-069200US			
Filed as Large Entity				
<b>Utility Filing Fees</b>				
<b>Description</b>	<b>Fee Code</b>	<b>Quantity</b>	<b>Amount</b>	<b>Sub-Total in USD(\$)</b>
<b>Basic Filing:</b>				
<b>Pages:</b>				
<b>Claims:</b>				
<b>Miscellaneous-Filing:</b>				
<b>Petition:</b>				
<b>Patent-Appeals-and-Interference:</b>				
<b>Post-Allowance-and-Post-Issuance:</b>				
<b>Extension-of-Time:</b>				
Extension - 2 months with \$0 paid	1252	1	450	450

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
<b>Miscellaneous:</b>				
Request for continued examination	1801	1	790	790
<b>Total in USD (\$)</b>				<b>1240</b>